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EFS ID:	3573498				
Application Number:	10807648				
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Title of Invention:	Cached memory system and cache controller for embedded digital signal processor				
First Named Inventor/Applicant Name:	Gilbert Christopher Sih				
Customer Number:	23696				
Filer:	Joseph B. Agusta/Theresa Boyce				
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			159041			
1	Amendment After Final	030459_RFOA_07-07-08.pdf		no	16	

Warnings:

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.